09/592106

U.S. PTO
06/12/00

709	104	ISSUE CLASSIFICATION
Class	Subclass	

PATENT NUMBER

U.S. **UTILITY** Patent Application

JC2
JC811

O.I.P.E.		PATENT DATE
SCANNED KW HT(1) Q.A. AH		

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/592106	D	712	2 3	2782 2783	Drngjin

APPLICANTS

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TITLE

Method and apparatus for implementing atomicity of memory operations in dynamic multi-streaming processors

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
709	104	712	225						
INTERNATIONAL CLASSIFICATION		711	152						
G06F	9/00	710	200						
G06F	7/38								
G06F	13/28								
G06F	12/00								

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	11	13	8	11	1

☐ The term of this patent subsequent to _____ (date), has been disclaimed.	(Assistant Examiner) (Date)	**NOTICE OF ALLOWANCE MAILED** 03/24/04
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	LARRY D. DONAGHUE PRIMARY EXAMINER (Primary Examiner) (Date)	**ISSUE FEE**
		Amount Due: # 665.00 Date Paid
☐ The terminal ____ months of this patent have been disclaimed.	(Legal Instruments Examiner) 03/25/04 (Date)	**ISSUE BATCH NUMBER**

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